

IN THE
UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor(s): Julie Ann Ward et al.

Confirmation No.: 1727

Application No.: 10/027,589

Examiner: Thong H. Vu

Filing Date: 12/19/2001

Group Art Unit: 2142

Title: Reliability for Interconnect Fabrics

Mail Stop Amendment
Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Sir:

This Information Disclosure Statement is submitted:

- ☐ under 37 CFR 1.97(b), or
(Within three months of filing national application; or date of entry of national application; or before mailing date of first office action on the merits; whichever occurs last)
- ☒ under 37 CFR 1.97 (c) together with either a:
 - ☐ Statement under 37 CFR 1.97(e), or
 - ☒ a \$180.00 fee under 37 CFR 1.17(p), or
(After the CFR 1.97 (b) time period, but before final action or notice of allowance, whichever occurs first)
- ☐ under 37 CFR 1.97 (d) together with a:
 - ☐ Statement under 37 CFR 1.97(e)(1) or (2), and
 - ☐ a \$180.00 fee set forth in 37 CFR 1.17(p).
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Please charge to Deposit Account 08-2025 the sum of \$180.00. At any time during the pendency of this application, please charge any fees required or credit any overpayment to Deposit Account 08-2025 pursuant to 37 CFR 1.25.

☒ Applicant(s) submit herewith Form PTO 1449 - Information Disclosure Statement together with any required copies of patents, publications or other information of which applicant(s) are aware, which applicant(s) believe(s) may be material to the examination of this application and for which there may be a duty to disclose in accordance with 37 CFR 1.56.

☐ A concise explanation of the relevance of foreign language patents, foreign language publications and other foreign language information listed on PTO Form 1449, as presently understood by the individual(s) designated in 37 CFR 1.56 (c) most knowledgeable about the content is given on the attached sheet, or where a foreign language patent is cited in a search report or other action by a foreign patent office in a counterpart foreign application, an English language version of the search report or action which indicates the degree of relevance found by the foreign office is listed on form PTO 1449 and is enclosed herewith.

It is requested that the information disclosed herein be made of record in this application.

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Respectfully submitted,

Julie Ann Ward et al.

By 

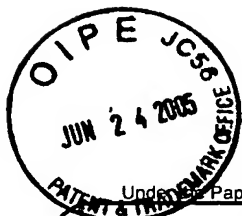
Derek J. Westberg

Attorney/Agent for Applicant(s)

Reg. No. 40,872

Date: June 22, 2005

Telephone No.: (408) 293-9000



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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

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Complete if Known

Application Number	10/027,589
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First Named Inventor	Julie Ann WARD
Art Unit	2142
Examiner Name	Thong H. VU
Attorney Docket Number	10019570-1

Sheet 1 of 7

U. S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			
	1A	US- 2002-0083159-A1	06-27-2002	Ward et al.	
	1B	US- 2002-0091804-A1	07-11-2002	Ward et al.	
	1C	US- 2002-0120770-A1	08-29-2002	Parham et al.	
	1D	US- 2002-0156828-A1	10-24-2002	Ishizaki et al.	
	1E	US- 2002-0188732-A1	12-12-2002	Buckman et al.	
	1F	US- 2003-0065758-A1	04-03-2003	O'Sullivan et al.	
	1G	US- 2003-0144822-A1	07-31-2003	Peh et al.	
	1H	US- 2003-0145294-A1	07-31-2003	Ward et al.	
	1I	US- 2005-0021583-A1	01-27-2005	Andrzejak et al.	
	1J	US- 2005-0021831-A1	01-27-2005	Andrzejak et al.	
	1K	US- 2005-0033844-A1	02-10-2005	Andrzejak et al.	
	1L	US- 5,138,657-A	08-11-1992	Colton et al.	
	1M	US- 5,245,609-A	09-14-1993	Ofek et al.	
	1N	US- 5,426,674-A	01-20-1995	Nemirovsky et al.	
	1O	US- 5,581,689-A	12-03-1996	Slominski et al.	
	1P	US- 5,598,532-A	01-28-1997	Liron	
	1Q	US- 5,649,105-A	07-15-1997	Aldred et al.	
	1R	US- 5,651,005-A	07-22-1997	Kwok et al.	
	1S	US- 5,793,362-A	08-11-1998	Matthews et al.	

FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear	T ⁶
		Country Code ³ -Number ⁴ -Kind Code ⁵ (if known)				
	1T	WO-96/17458-A	06-06-1996	British		
				Telecommunications		
				Public Limited		
				Company		

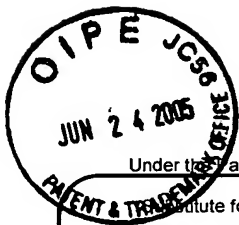
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		Number-Kind Code ² (if known)			
	2A	US- 5,805,578-A	09-08-1993	Stirpe et al.	
	2B	US- 5,815,402-A	09-29-1998	Taylor et al.	
	2C	US- 5,838,919-A	11-17-1998	Schwaller et al.	
	2D	US- 5,987,517-A	11-16-1999	Firth et al.	
	2E	US- 6,003,037-A	12-14-1999	Kassabgi et al.	
	2F	US- 6,031,984-A	02-29-2000	Walser	
	2G	US- 6,038,219-A	03-14-2000	Mawhinney et al.	
	2H	US- 6,047,199-A	04-04-2000	DeMarco	
	2I	US- 6,052,360-A	04-18-2000	Rogers	
	2J	US- 6,108,782-A	08-22-2000	Fletcher et al.	
	2K	US- 6,141,355-A	10-31-2000	Palmer et al.	
	2L	US- 6,157,645-A	12-05-2000	Shobatake	
	2M	US- 6,212,568-B1	04-03-2001	Miller et al.	
	2N	US- 6,331,905-B1	12-18-2001	Ellinas et al.	
	2O	US- 6,345,048-B1	02-05-2002	Allen et al.	
	2P	US- 6,363,334-B1	03-26-2002	Andrews et al.	
	2Q	US- 6,418,481-B1	07-09-2002	Mancusi et al.	
	2R	US- 6,442,584-B1	08-27-2002	Kolli et al.	
	2S	US- 6,452,924-B1	09-17-2002	Golden et al.	

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		Number-Kind Code ² (if known)			
	3A	US- 6,526,240-B2	02-25-2003	Borowosky et al.	
	3B	US- 6,539,027-B1	03-25-2003	Cambren	
	3C	US- 6,539,531-B2	03-25-2003	Miller et al.	
	3D	US- 6,570,850-B1	05-27-2003	Gutierrez et al.	
	3E	US- 6,598,080-B1	07-22-2003	Nagami et al.	
	3F	US- 6,625,777-B1	09-23-2003	Levin et al.	
	3G	US- 6,633,909-B1	10-14-2003	Barrett et al.	
	3H	US- 6,650,639-B2	11-18-2003	Doherty et al.	
	3I	US- 6,668,308-B2	12-23-2003	Barroso et al.	
	3J	US- 6,675,328-B1	01-06-2004	Krishnamachari et al.	
	3K	US- 6,687,222-B1	02-03-2004	Albert et al.	
	3L	US- 6,694,361-B1	02-17-2004	Shah et al.	
	3M	US- 6,697,334-B1	02-24-2004	Klincewicz et al.	
	3N	US- 6,697,369-B1	02-24-2004	Dziong et al.	
	3O	US- 6,697,854-B1	02-24-2004	Glassen et al.	
	3P	US- 6,724,757-B1	04-20-2004	Zadikian et al.	
	3Q	US- 6,744,767-B1	06-01-2004	Chiu et al.	
	3R	US- 6,757,731-B1	06-29-2004	Barnes et al.	
	3S	US- 6,778,496-B1	08-17-2004	Meempat et al	

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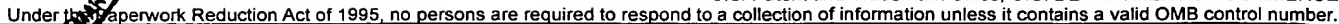
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Sheet	4	of	7
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NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	5A	Ravindra K. AHUJA et al., Network Flows: Theory, Algorithms, and Applications, pp. 4-9, 649-686, 1993, Prentice-Hall, Upper Saddle River, NJ.	
	5B	Dimitri P. BERTSEKAS, Linear Network Optimization: Algorithms and Codes, pages 253-260, 1991, The MIT Press, Cambridge, MA.	
	5C	Robert E. BIXBY, Solving Real-World Linear Programs: A Decade and More of Progress, January 2002.	
	5D	Viraphol CHAIYAKUL, Assignment Decision Diagram for High-Level Synthesis, Technical Report #92-103, 1992, Department of Information and Computer Science, University of California, Irvine, CA.	
	5E	Andre DEHON, Notes on Coupling Processors with Reconfigurable Logic, MIT Transit Project, Transit Note #118, 1995.	
	5F	Julie Ward DREW et al., US Patent Application No. 10/290,760, Filing Date: November 8, 2002.	
	5G	Julie Ward DREW, US Patent Application No. 10/290,643, Filing Date: November 8, 2002.	
	5H	Julie Ward DREW et al., US Patent Application No. 10/845,855, Filing Date: May 13, 2004.	
	5I	Christodoulos A. FLOUDAS et al., Quadratic Optimization, 1995.	
	5J	Cathy FULTON et al., Impact Analysis of Packet-Level Scheduling On an ATM Shared-Memory Switch, NFOCOM '98. Proceedings of Seventeenth Annual Joint Conference of the IEEE Computer and Communications Societies, Vol. 3, pp. 947-954, 1998, IEEE, New York, NY.	

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Sheet	6	of	7

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Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	6A	Hiroshi INOSE, An Introduction to Digital Integrated Communication Systems, pages 87-90, 1979, Peter Peregrinus Ltd., Stevenage, United Kingdom.	
	6B	D. KLINGMAN et al. Netgen: A Program for Generating Large Scale Capacitated Assignment, Transportation, and Minimum Cost Flow Network Problems, Management Science 20(5):814-821, 1974, The Institute Of Management Sciences, Providence, RI.	
	6C	A. Richard NEWTON, Interface-Based Design; Introduction, University of California at Berkeley, April 1999.	
	6D	Matthew T. O'KEEFE et al., Designing Fibre Channel Storage Area Networks, February 2001.	
	6E	Derek C. W. PAO, A Congestion Control Algorithm for Multipoint-to-Multipoint ABR Service in ATM Network, Proceedings of the IEEE Conference on High Performance Switching and Routing, June 26, 2000, pp. 167-175, IEEE, New York, NY.	
	6F	Parameswaran RAMANATHAN et al., Resource Placement with Multiple Adjacency Constraints in k-ary n-Cubes, 1995.	
	6G	Staffan Bo STRAND, Storage Area Networks and SANTK, Thesis, December 2001, University of Minnesota, Minneapolis, MN.	
	6H	Staffan STRAND, Automatic Generation of Core/Edge Topology SANs Using SANTK, May 23, 2002.	
	6I	Joachim P. WALSER, Solving Linear Pseudo-Boolean Constraint Problems with Local Search, Proceedings of the 14th National Conference on Artificial Intelligence, 1997, pp. 269-274, AAAI Press, Menlo Park, CA.	
	6J	Julie WARD, US Patent Application No. 09/707,227, Filing Date: November 6, 2000.	

Examiner Signature		Date Considered	
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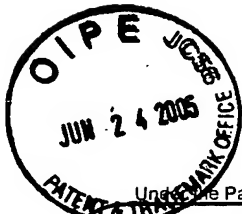
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Application Number	10/027,589
Filing Date	December 19, 2001
First Named Inventor	Julie Ann WARD
Art Unit	2142
Examiner Name	Thong H. VU
Attorney Docket Number	10019570-1

Sheet

7

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7

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	7A	Julie WARD et al., Storage Area Network Fabric Design, PowerPoint presentation given at the Math Sciences Research Institute for Combinatorial Design, Berkeley CA, November 8, 2000.	
	7B	I. WIDJAJA et al., Performance Issues in VC-Merge Capable ATM LSRs, RFC 2682, September 1999, The Internet Society, Reston, VA.	

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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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